

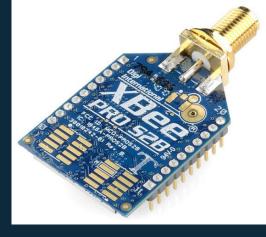
Critical Design Review Presentation

Advisor: Dr. Anthony Kuh Mentor: Tryston Fagarang Savath Saepoo EE 496 11/12/16



Overview

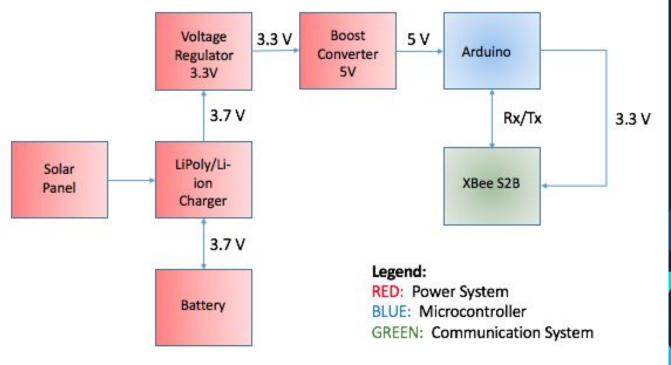
- Block Diagram
- Progress
- Remaining Tasks
- Questions





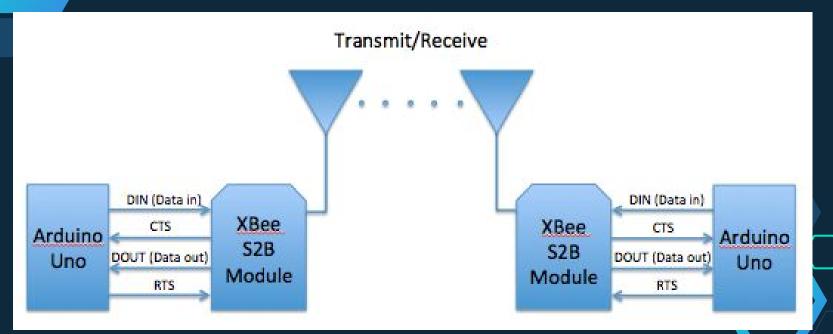


Overall Block Diagram





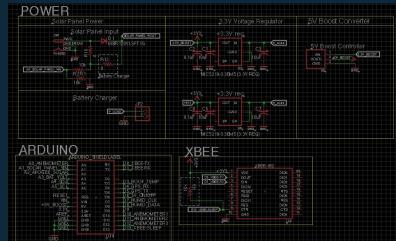
System Data Flow Diagram

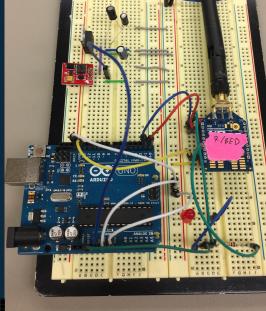




Progress

- Tested design on breadboard
- Reduced voltage regulator usage of 2 to 1
- Communication between coordinator XBee (XCTU) and router XBee (Breadboard)
- Created schematic of breadboard design on EAGLE



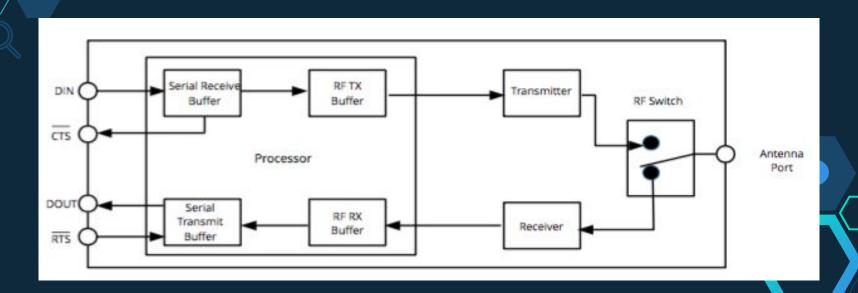






Issue

Serial buffer of the XBee module





Remaining Tasks

- Two way communication using one XBee (Router XBee)
- Finish PCB board design on EAGLE
 - Test board
- Housing



Gantt Chart

Week	1	2	3	4	5	6	7	8	9	10	11	12	13
Date	9/10/16	9/17/16	9/24/16	10/1/16	10/8/16	10/15/16	10/22/16	10/29/16	11/5/16	11/12/16	11/19/16	11/26/16	12/3/16
Presentations		- 20		9						- 2		97	(.1 *
Proposal		100		9			3	3		- 10		97	
Preliminary Design Review		- V2		9	4			3		- 10		97	
Critical Design Review		2		9								97	
Final Presentation		- VS		9				3		10		97	
Ant		- VS		9				3		- 10		97	
Reading Documentations	Y	1		4	4				The state of the s	1		100	
Configuring XBees on XCTU	, , , , , , , , , , , , , , , , , , ,			30								97	(.) ()
Design Layout				0.				3		- 10		97	
Testing Breadboard Design		- 20		4	1					- 10		97	
Debugging Breadboard Design		70		9	4					- 10		97	
Programming Arduino				9								97	
Board Fabrication		- 20		9				7				97	
Testing PCB Design		100		9		ě.	l	¥ 3		- 10		97	



Any Questions?



References

Websites:

 http://www.digi.com/products/xbee-rf-solutions/xctu-softwar e/xctu

Images:

- <u>https://cdn.sparkfun.com//assets/parts/4/8/9/3/10419-01.jpg</u>